

<b>PCN Number:</b>	20190909000	<b>PCN Date:</b>	Sept. 10, 2019
<b>Title:</b>	Datasheet for TPS2662		
<b>Customer Contact:</b>	PCN Manager	<b>Dept:</b>	Quality Services
<b>Change Type:</b>			
<input type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Design
<input type="checkbox"/>	Assembly Process	<input checked="" type="checkbox"/>	Data Sheet
<input type="checkbox"/>	Assembly Materials	<input type="checkbox"/>	Part number change
<input type="checkbox"/>	Mechanical Specification	<input type="checkbox"/>	Test Site
<input type="checkbox"/>	Packing/Shipping/Labeling	<input type="checkbox"/>	Test Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Site
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Material
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Site
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Materials
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Process

### Notification Details

#### Description of Change:

Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is being updated as summarized below. The following change history provides further details.



**TPS2662**

SLVSDT4E –OCTOBER 2017–REVISED AUGUST 2019

Changes from Revision D (February 2019) to Revision E	Page
• Changed from UL 2367 Recognition Pending to UL 2367 Recognized IEC 62368-1 Certified in the <b>Features</b> section	1
• Replaced TPS26623 with TPS26624 for Pin No. 4 SHDN in the <b>Pin Functions</b> table	4
• Added UVLO Recovery Time in the <b>Timing Requirements</b> table in the <b>Specifications</b> section	6
• Changed Input voltage range MAX from 60 V to 62 V in the <b>Absolute Maximum Ratings</b> table in the <b>Specifications</b> section	6
• Changed Input voltage MAX from 57 V to 60 V in the <b>Recommended Operating Conditions</b> table in the <b>Specifications</b> section	6
• Updated the <b>Parameter Measurement Information</b> graph to explain the UVLO <sub>t<sub>REC</sub></sub>	15
• Updated the <b>Feature Description Undervoltage Lockout (UVLO)</b> section to explain the UVLO <sub>t<sub>REC</sub></sub> timer	18
• Removed UVLO from the <b>Overload Protection</b> section	22

The datasheet number will be changing.

Device Family	Change From:	Change To:
TPS2662	SLVSDT4D	SLVSDT4E

These changes may be reviewed at the datasheet links provided.

<http://www.ti.com/product/TPS2662>

#### Reason for Change:

To accurately reflect device characteristics.

#### Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.

#### Changes to product identification resulting from this PCN:

None.

#### Product Affected:

TPS26620DRCR	TPS26620DRCT	TPS26624DRCR	TPS26624DRCT
TPS26622DRCR	TPS26622DRCT		

For questions regarding this notice, e-mails can be sent to the contacts shown below or your local Field Sales Representative.

<b>Location</b>	<b>E-Mail</b>
USA	<a href="mailto:PCNAmericasContact@list.ti.com">PCNAmericasContact@list.ti.com</a>
Europe	<a href="mailto:PCNEuropeContact@list.ti.com">PCNEuropeContact@list.ti.com</a>
Asia Pacific	<a href="mailto:PCNAsiaContact@list.ti.com">PCNAsiaContact@list.ti.com</a>
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